

SPECIFICATION FOR APPROVAL

DESCRIPTION: 7.0" LCD Module _____

CUSTOMER: _____

Product No: BR070JII 2757-B4 V.1 _____

Released Date: 2020. 11 .26 _____

Revision: _____

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| APPROVED SIGNATURES | | | |
|---------------------|--|--|--|
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Record of Revision

| Version | Revise Date | Page | Content |
|---------|-------------|------|-----------------|
| .01 | 2020/11/26 | | Initial Release |

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1. General Specifications

| No. | Item | Specification | Remark |
|-----|-----------------------------|---------------------------------|--------|
| 1 | LCD size | 7.0 inch(Diagonal) | |
| 2 | Driver element | a-Si TFT active matrix | |
| 3 | Resolution | 1024 × 3(RGB) × 600 | |
| 4 | Display mode | Normally Black,IPS | |
| 5 | Dot pitch | 0.0502(W) × 0.1432(H) mm | |
| 6 | Active area | 154.21(W) × 85.92(H) mm | |
| 7 | Module size | 164.9(W) × 100.0(H) × 5.7(D) mm | Note 1 |
| 8 | Surface treatment | Anti-Glare | |
| 9 | Color arrangement | RGB-stripe | |
| 10 | Interface | RGB interface | |
| 11 | Backlight power consumption | TBD | |
| 12 | Panel power consumption | TBD | |
| 13 | Weight | TBD | |

Note 1: Refer to Mechanical Drawing.

2. Pin Assignment

TFT LCD Panel Driving Section

FPC Connector is used for the module electronics interface. The recommended model is FH12A-50S-0.5SH manufactured by Hirose.

| Pin No. | Symbol | I/O | Function | Remark |
|---------|-------------------|-----|-----------------------------------|--------|
| 1 | V _{LED+} | P | Power for LED backlight (Anode) | |
| 2 | V _{LED+} | P | Power for LED backlight (Anode) | |
| 3 | V _{LED-} | P | Power for LED backlight (Cathode) | |
| 4 | V _{LED-} | P | Power for LED backlight (Cathode) | |
| 5 | GND | P | Power ground | |
| 6 | V _{COM} | I | Common voltage | |
| 7 | DV _{DD} | P | Power for Digital Circuit | |
| 8 | MODE | I | DE/SYNC mode select | Note 1 |
| 9 | DE | I | Data Input Enable | |
| 10 | VS | I | Vertical Sync Input | |
| 11 | HS | I | Horizontal Sync Input | |
| 12 | B7 | I | Blue data(MSB) | |
| 13 | B6 | I | Blue data | |
| 14 | B5 | I | Blue data | |
| 15 | B4 | I | Blue data | |
| 16 | B3 | I | Blue data | |
| 17 | B2 | I | Blue data | |
| 18 | B1 | I | Blue data | Note 2 |
| 19 | B0 | I | Blue data(LSB) | Note 2 |
| 20 | G7 | I | Green data(MSB) | |
| 21 | G6 | I | Green data | |
| 22 | G5 | I | Green data | |
| 23 | G4 | I | Green data | |
| 24 | G3 | I | Green data | |
| 25 | G2 | I | Green data | |

| | | | | |
|----|------------------|---|--------------------------|----------|
| 26 | G1 | I | Green data | Note 2 |
| 27 | G0 | I | Green data(LSB) | Note 2 |
| 28 | R7 | I | Red data(MSB) | |
| 29 | R6 | I | Red data | |
| 30 | R5 | I | Red data | |
| 31 | R4 | I | Red data | |
| 32 | R3 | I | Red data | |
| 33 | R2 | I | Red data | |
| 34 | R1 | I | Red data | Note 2 |
| 35 | R0 | I | Red data(LSB) | Note 2 |
| 36 | GND | P | Power Ground | |
| 37 | DCLK | I | Sample clock | Note 3 |
| 38 | GND | P | Power Ground | |
| 39 | L/R | I | Left / right selection | Note 4,5 |
| 40 | U/D | I | Up/down selection | Note 4,5 |
| 41 | V _{GH} | P | Gate ON Voltage | |
| 42 | V _{GL} | P | Gate OFF Voltage | |
| 43 | AV _{DD} | P | Power for Analog Circuit | |
| 44 | RESET | I | Global reset pin. | Note 6 |
| 45 | NC | - | No connection | |
| 46 | V _{COM} | I | Common Voltage | |
| 47 | DITHB | I | Dithering function | Note 7 |
| 48 | GND | P | Power Ground | |
| 49 | NC | - | No connection | |
| 50 | NC | - | No connection | |

I: input, O: output, P: Power

Note 1: DE/SYNC mode select. Normally pull high.

When select DE mode, MODE="1", VS and HS must pull high.

When select SYNC mode, MODE="0", DE must be grounded.

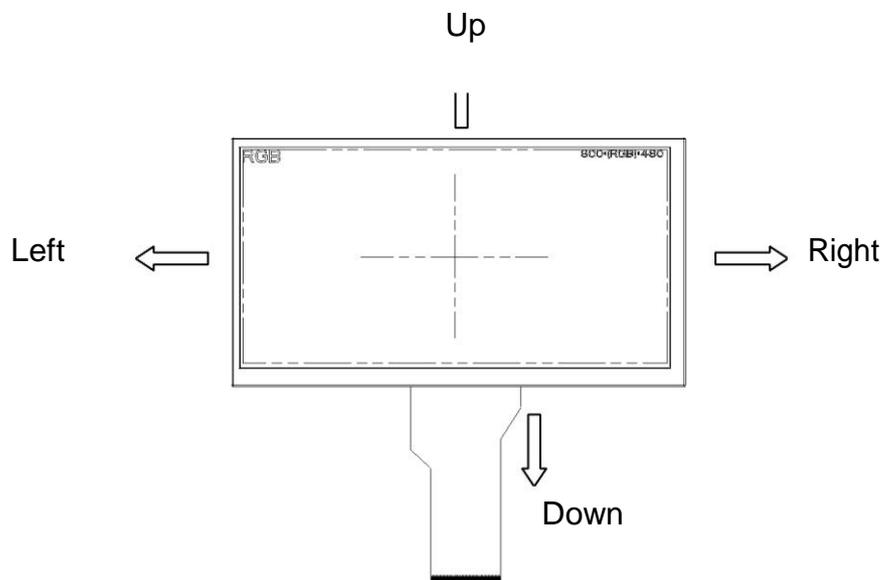
Note 2: When input 18 bits RGB data, the two low bits of R,G and B data must be grounded.

Note 3: Data shall be latched at the falling edge of DCLK.

Note 4: Selection of scanning mode

| Setting of scan control input | | Scanning direction |
|-------------------------------|------------------|---------------------------|
| U/D | L/R | |
| GND | DV _{DD} | Up to down, left to right |
| DV _{DD} | GND | Down to up, right to left |
| GND | GND | Up to down, right to left |
| DV _{DD} | DV _{DD} | Down to up, left to right |

Note 5: Definition of scanning direction.
Refer to the figure as below:



Note 6: Global reset pin. Active low to enter reset state. Suggest to connect with an RC reset circuit for stability. Normally pull high.

Note 7: Dithering function enable control, normally pull high.
When DITHB="1", Disable internal dithering function,
When DITHB="0", Enable internal dithering function,

3. Operation Specifications

3.1. Absolute Maximum Ratings

(Note 1)

| Item | Symbol | Values | | Unit | Remark |
|-----------------------|----------------------------------|--------|------|------|--------------------|
| | | Min. | Max. | | |
| Power voltage | DV _{DD} | -0.3 | 5.0 | V | |
| | AV _{DD} | 6.5 | 13.5 | V | |
| | V _{GH} | -0.3 | 40.0 | V | |
| | V _{GL} | -20.0 | 0.3 | V | |
| | V _{GH} -V _{GL} | - | 40.0 | V | |
| Operation Temperature | T _{OP} | -20 | 70 | °C | |
| Storage Temperature | T _{ST} | -30 | 80 | °C | |
| LED Reverse Voltage | V _R | - | 1.2 | V | Each LED Note 2 |
| LED Forward Current | I _F | - | 25 | mA | Each LED |

Note 1: The absolute maximum rating values of this product are not allowed to be exceeded at any times. Should a module be used with any of the absolute maximum ratings exceeded, the characteristics of the module may not be recovered, or in an extreme case, the module may be permanently destroyed.

Note 2: V_R Conditions: Zener Diode 20mA

3.1.1. Typical Operation Conditions

(Note 1)

| Item | Symbol | Values | | | Unit | Remark |
|--------------------------|------------------|----------------------|------|----------------------|------|--------|
| | | Min. | Typ. | Max. | | |
| Power voltage | DV _{DD} | 3.0 | 3.3 | 3.6 | V | Note 2 |
| | AV _{DD} | 12.0 | 12.5 | 12.7 | V | |
| | V _{GH} | 16.0 | 17.0 | 18.0 | V | |
| | V _{GL} | -7.5 | -7.0 | -6.5 | V | |
| Input signal voltage | V _{COM} | 3.0 | 3.15 | 3.3 | V | |
| Input logic high voltage | V _{IH} | 0.7 DV _{DD} | - | DV _{DD} | V | Note 3 |
| Input logic low voltage | V _{IL} | 0 | - | 0.3 DV _{DD} | V | |

Note 1: Be sure to apply DV_{DD} and V_{GL} to the LCD first, and then apply V_{GH}.

Note 2: DV_{DD} setting should match the signals output voltage (refer to Note 3) of customer's system board.

Note 3: DCLK,HS,VS,RESET,U/D, L/R,DE,R0~R7,G0~G7,B0~B7,MODE,DITHB.

3.1.2. Current Consumption

| Item | Symbol | Values | | | Unit | Remark |
|--------------------|-------------------|--------|------|------|------|--------------------------|
| | | Min. | Typ. | Max. | | |
| Current for Driver | I _{GH} | - | 0.2 | 1.0 | mA | V _{GH} =17.0V |
| | I _{GL} | - | 0.2 | 1.0 | mA | V _{GL} = -7.0V |
| | ID _{VDD} | - | 6.0 | 10 | mA | DV _{DD} =3.3V |
| | IA _{VDD} | - | 20 | 50 | mA | AV _{DD} = 12.5V |

3.1.3. Backlight Driving Conditions

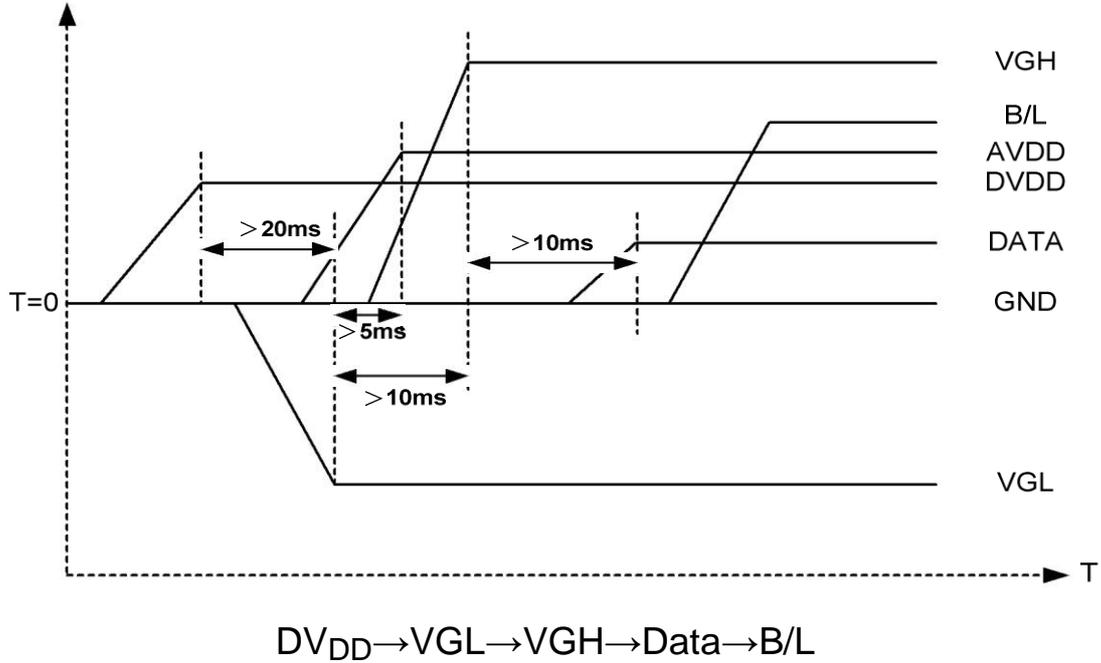
| Item | Symbol | Values | | | Unit | Remark |
|---------------------------|----------------|--------|-------|-------|------|--------|
| | | Min. | Typ. | Max. | | |
| Voltage for LED backlight | V _L | (8.4) | (9.0) | (9.6) | V | Note 1 |
| Current for LED backlight | I _L | (270) | (300) | (330) | mA | |
| LED life time | - | 25,000 | - | - | Hr | Note 2 |

Note 1: The LED Supply Voltage is defined by the number of LED at Ta=25°C and I_L =300mA.

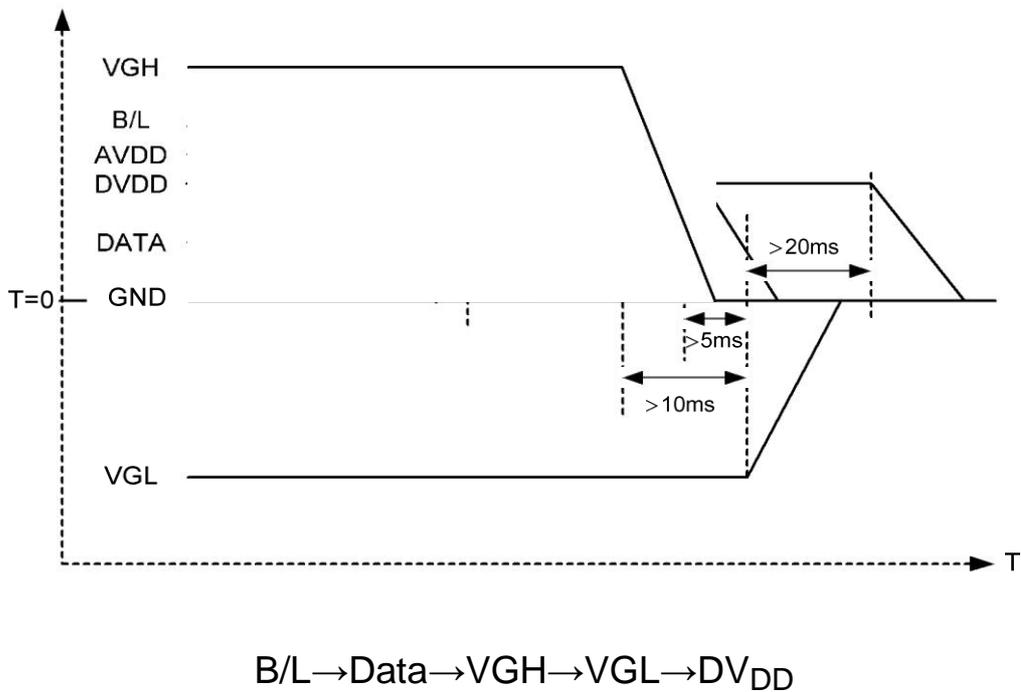
Note 2: The “LED life time” is defined as the module brightness decrease to 50% original brightness at Ta=25°C and I_L =300mA. The LED lifetime could be decreased if operating I_L is lager than 330mA.

3.2. Power Sequence

a. Power on:



b. Power off:



Note: Data include R0~R7, B0~B7, GO~G7, U/D, L/R, DCLK, HS,VS,DE.

3.3. Timing Characteristics

3.3.1. AC Electrical Characteristics

| Item | Symbol | Values | | | Unit | Remark |
|-------------------------------------|-----------|--------|------|------|------|-----------------------------------|
| | | Min. | Typ. | Max. | | |
| HS setup time | T_{hst} | 8 | - | - | ns | |
| HS hold time | T_{hhd} | 8 | - | - | ns | |
| VS setup time | T_{vst} | 8 | - | - | ns | |
| VS hold time | T_{vhd} | 8 | - | - | ns | |
| Data setup time | T_{dsu} | 8 | - | - | ns | |
| Data hole time | T_{dhd} | 8 | - | - | ns | |
| DE setup time | T_{esu} | 8 | - | - | ns | |
| DE hole time | T_{ehd} | 8 | - | - | ns | |
| DV _{DD} Power On Slew rate | T_{POR} | - | - | 20 | ms | From 0 to 90% DV _{DD} |
| RESET pulse width | T_{Rst} | 1 | - | - | ms | |
| DCLK cycle time | T_{coh} | 20 | - | - | ns | |
| DCLK pulse duty | T_{cwh} | 40 | 50 | 60 | % | |

3.3.2. Data Input Format

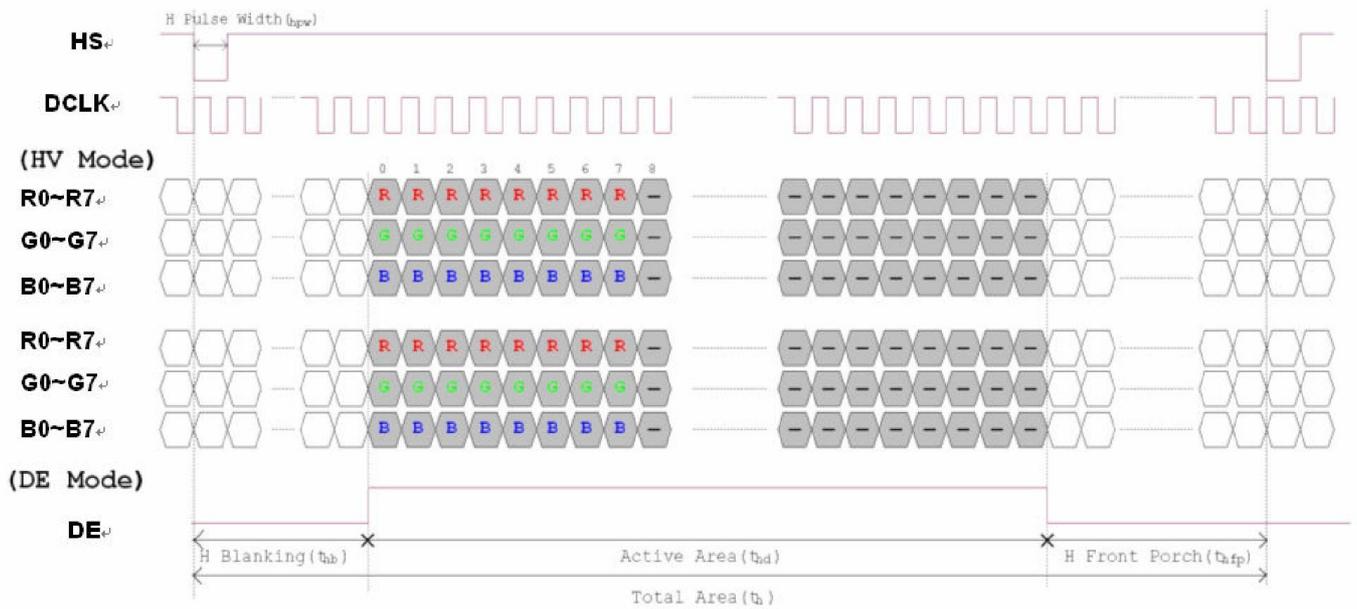


Figure 3. 1 Horizontal input timing diagram.

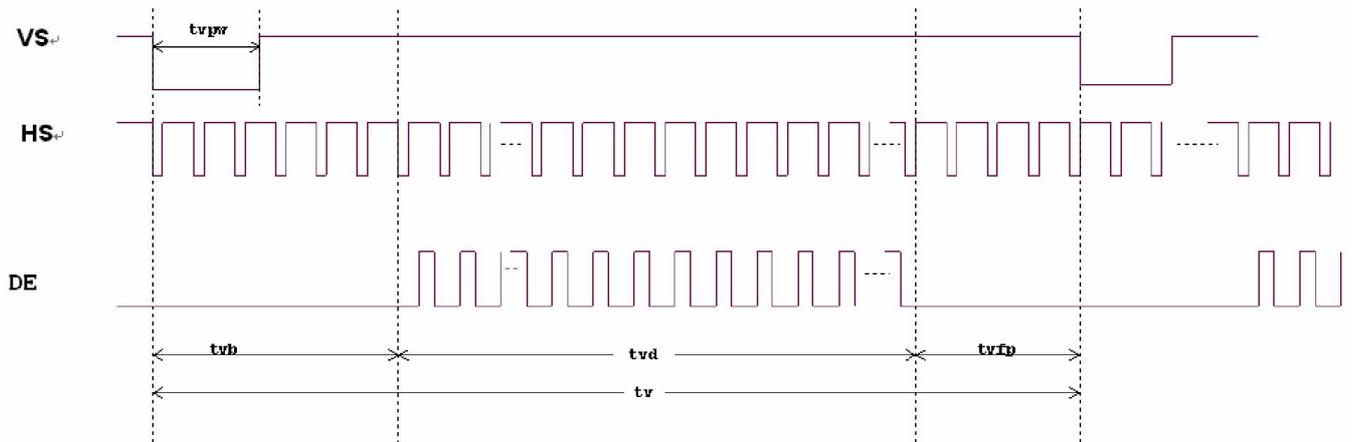


Figure 3. 2 Vertical input timing diagram.

3.3.3. Timing

| Item | Symbol | Values | | | Unit | Remark |
|-------------------------|--------|--------|------|------|------|--------|
| | | Min. | Typ. | Max. | | |
| Horizontal Display Area | thd | - | 1024 | - | DCLK | |
| DCLK Frequency | fclk | 44.9 | 51.2 | 63.0 | MHz | |
| One Horizontal Line | th | 1200 | 1344 | 1400 | DCLK | |
| HS pulse width | thpw | 1 | - | 140 | DCLK | |
| HS Blanking | thb | 160 | 160 | 160 | DCLK | |
| HS Front Porch | thfp | 16 | 160 | 216 | DCLK | |

| Item | Symbol | Values | | | Unit | Remark |
|-----------------------|--------|--------|------|------|------|--------|
| | | Min. | Typ. | Max. | | |
| Vertical Display Area | tvd | - | 600 | - | TH | |
| VS period time | tv | 624 | 635 | 750 | TH | |
| VS pulse width | tvpw | 1 | - | 20 | TH | |
| VS Blanking | tvb | 23 | 23 | 23 | TH | |
| VS Front Porch | tvfp | 1 | 12 | 127 | TH | |

4. Optical Specifications

| Items | Symbol | Condition | Min. | Typ. | Max. | Unit | Remark | |
|----------------|-----------------------------------|------------------------|--|-------|-------|---------|-----------------|--|
| Viewing angles | θ_T | Center CR \geq 10 | 80 | 85 | - | Degree. | Note2 | |
| | θ_B | | 80 | 85 | - | | | |
| | θ_L | | 80 | 85 | - | | | |
| | θ_R | | 80 | 85 | - | | | |
| Contrast Ratio | CR | $\Theta = 0$ | | 800 | - | - | Note1, Note3 | |
| Response Time | T _{ON} +T _{off} | 25°C | - | 25 | 35 | ms | Note1, Note4 | |
| Chromaticity | Red | R _X | $\Theta = 0$ Normal viewing angle | 0.510 | 0.560 | 0.610 | - | |
| | | R _Y | | 0.295 | 0.345 | 0.395 | - | |
| | Green | G _X | | 0.273 | 0.323 | 0.373 | | |
| | | G _Y | | 0.549 | 0.599 | 0.649 | | |
| | Blue | B _X | | 0.102 | 0.152 | 0.202 | | |
| | | B _Y | | 0.066 | 0.116 | 0.166 | | |
| | White | W _X | | 0.260 | 0.310 | 0.360 | | |
| | | W _Y | | 0.280 | 0.330 | 0.380 | | |
| Uniformity | U | | 70 | 75 | - | % | Note1, Note6 | |
| Luminance | L | | 400 | 450 | | | Note1, Note7 | |

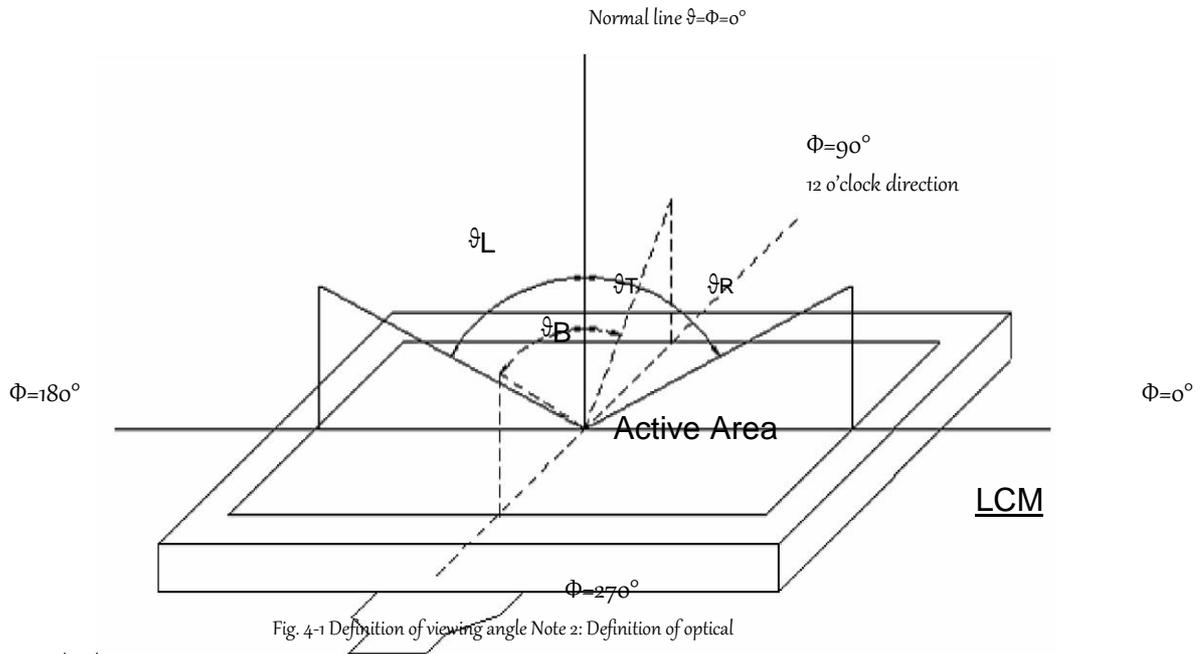
Test Conditions:

1. IF= 33mA(one channel),the ambient temperature is 25°C
2. The test systems refer to Note 1 and Note 2.

Note 1:Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 5 minutes operation, the optical properties are measured at the center point of the LCD screen. All input terminals LCD panel must be ground when measuring the center area of the panel.

Note 1: Definition of viewing angle range



The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/Field of view: 1° /Height: 500mm.)

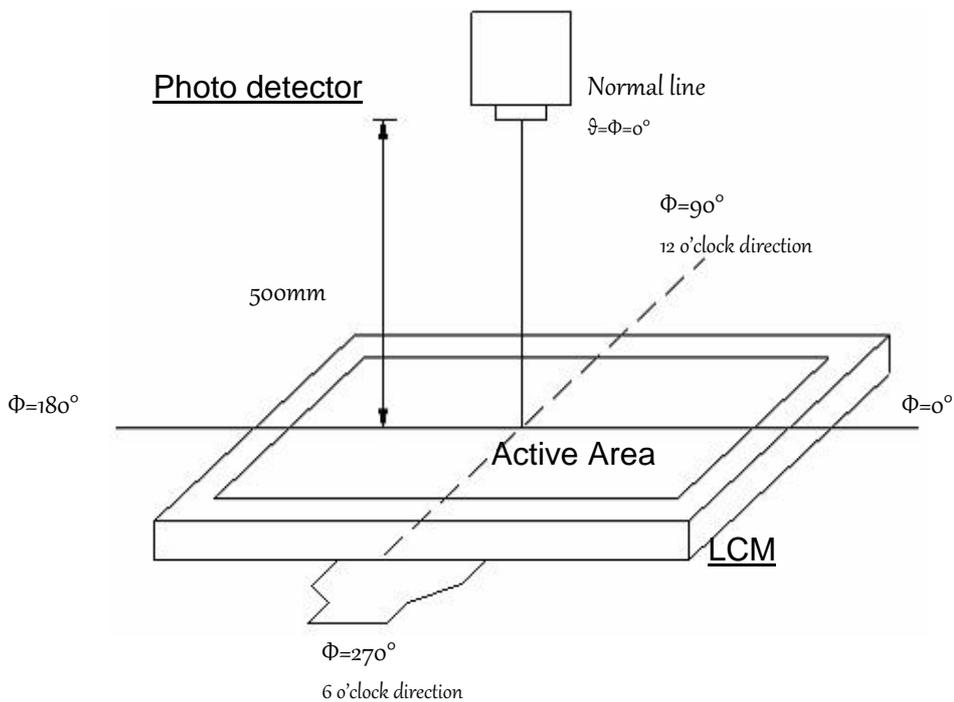


Fig. 4-2 Optical measurement system setup

Note 3: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time (T_{ON}) is the time between photo detector output intensity changed from 90% to 10%. And fall time (T_{OFF}) is the time between photo detector output intensity changed from 10% to 90%.

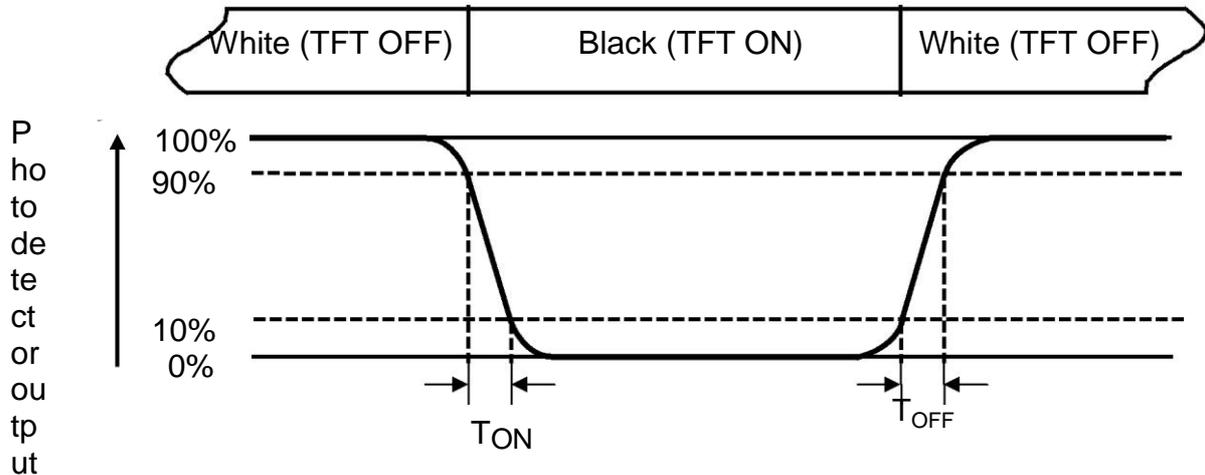


Fig. 4-3 Definition of response time

Note 4: Definition of contrast ratio

$$\text{Contrast ratio (CR)} = \frac{\text{Luminance measured when LCD on the "White" state}}{\text{Luminance measured when LCD on the "Black" state}}$$

Note 5: Definition of color chromaticity (CIE1931)

Color coordinates measured at center point of LCD.

Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is $I_L=300\text{mA}$.

Note 7: Definition of Luminance Uniformity

Active area is divided into 9 measuring areas (Refer to Fig. 4-4).Every measuring point is placed at the center of each measuring area.

$$\text{Luminance Uniformity (Yu)} = \frac{B_{\min}}{B_{\max}}$$

L-----Active area length W----- Active area width

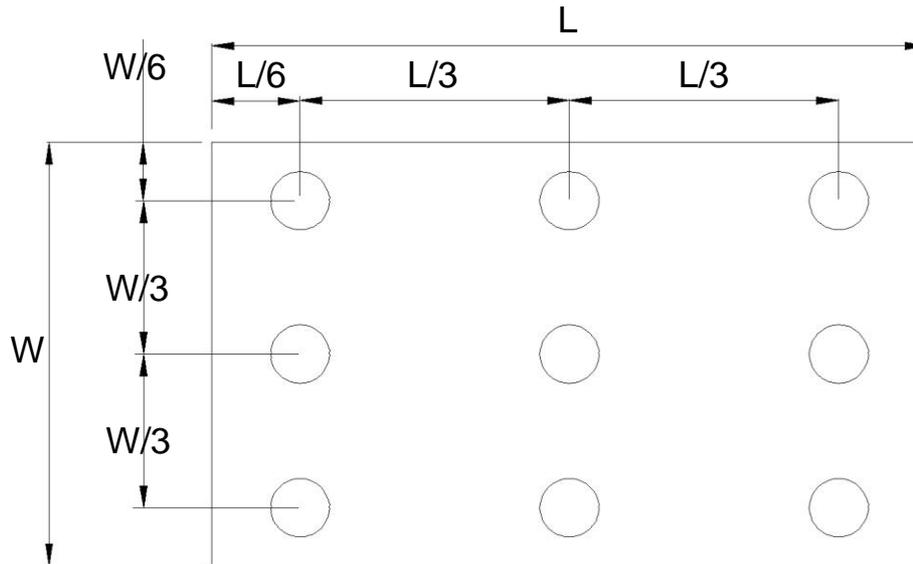


Fig. 4-4 Definition of measuring points

B_{\max} : The measured maximum luminance of all measurement position.

B_{\min} : The measured minimum luminance of all measurement position.

6. General Precautions

6.1. Safety

Liquid crystal is poisonous. Do not put it in your mouth. If liquid crystal touches your skin or clothes, wash it off immediately by using soap and water.

6.2. Handling

1. The LCD panel is plate glass. Do not subject the panel to mechanical shock or to excessive force on its surface.
2. The polarizer attached to the display is easily damaged. Please handle it carefully to avoid scratch or other damages.
3. To avoid contamination on the display surface, do not touch the module surface with bare hands.
4. Keep a space so that the LCD panels do not touch other components.
5. Put cover board such as acrylic board on the surface of LCD panel to protect panel from damages.
6. Transparent electrodes may be disconnected if you use the LCD panel under environmental conditions where the condensation of dew occurs.
7. Do not leave module in direct sunlight to avoid malfunction of the ICs.

6.3. Static Electricity

1. Be sure to ground module before turning on power or operating module.
2. Do not apply voltage which exceeds the absolute maximum rating value.

6.4. Storage

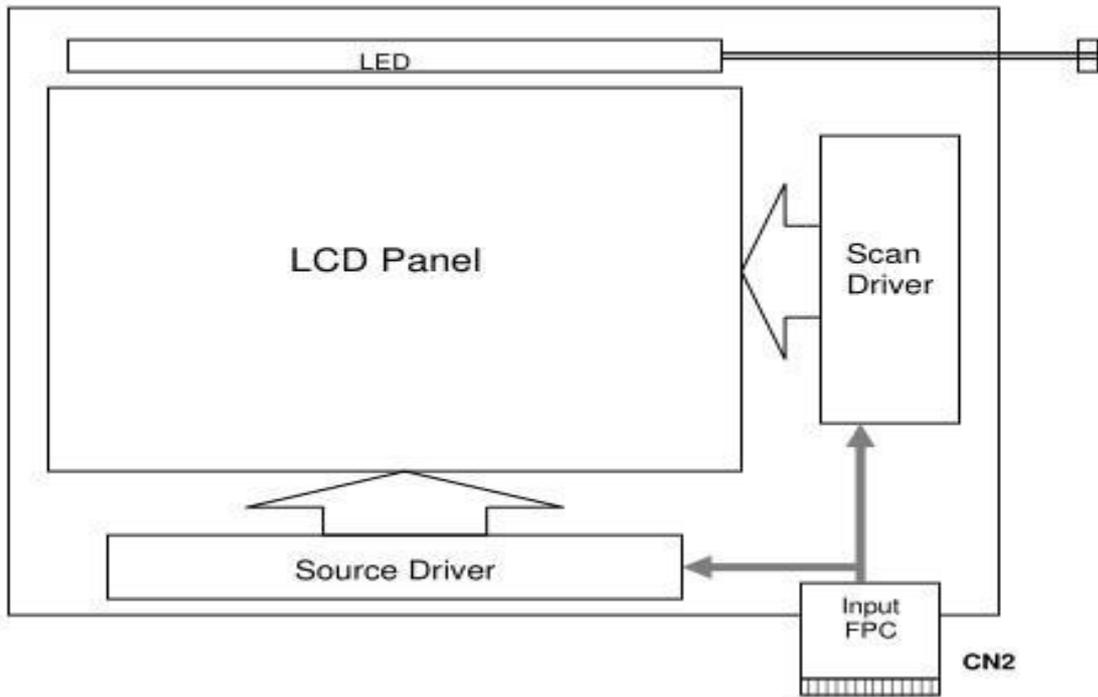
1. Store the module in a dark room where must keep at $25\pm 10^{\circ}\text{C}$ and 65%RH or less.
2. Do not store the module in surroundings containing organic solvent or corrosive gas.
3. Store the module in an anti-electrostatic container or bag.

6.5. Cleaning

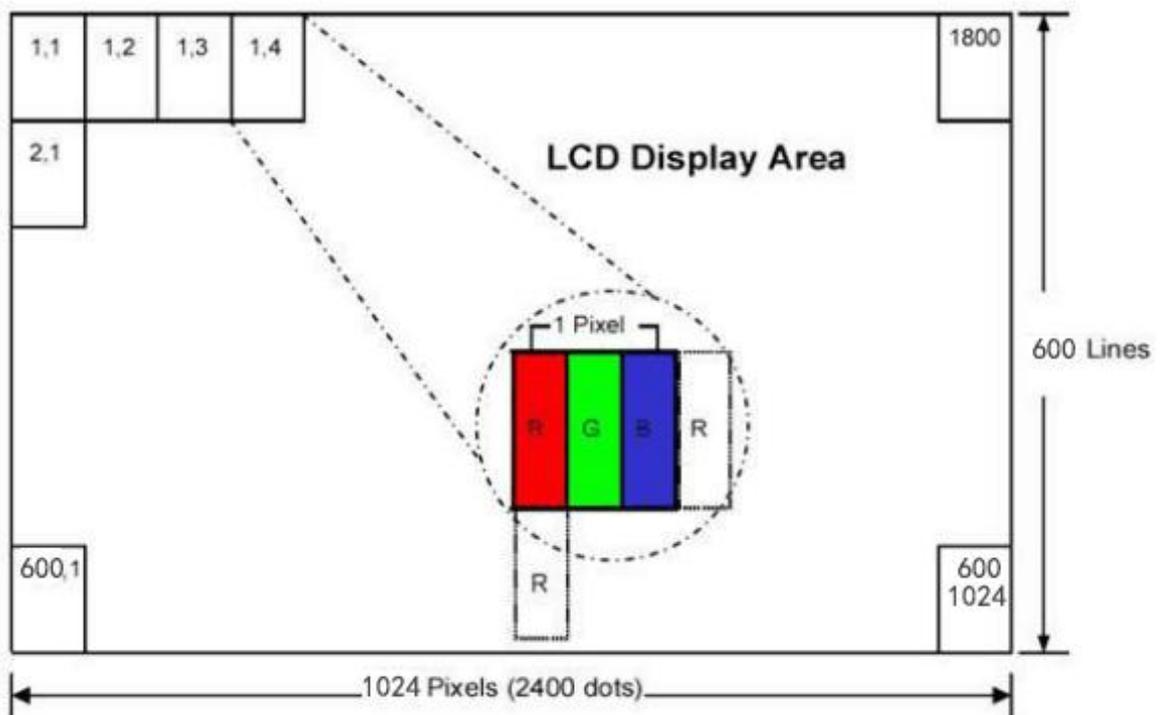
1. Do not wipe the polarizer with dry cloth. It might cause scratch.
2. Only use a soft sloth with IPA to wipe the polarizer, other chemicals might permanent damage to the polarizer.

7.0 BLOCK DIAGRAM

7.1 TFT LCD Module



7.2 Pixel Format



8 Mechanical Drawing

NOTES:

1. GENERAL TOLERANCE: ±0.2
2. () REFERENCE DIMENSION
3. RECOMMENDED CUSHION ADHERENT ARKE: TP V, A+1, 6mm
4. REQUIREMENTS ON ENVIRONMENTAL PROTECTION: RoHS

| NO. | SYMBOL | DESCRIPTION | DATE | APPROVAL | DESIGNER | DATE | APPROVAL |
|-----|--------|-------------|------------|----------|------------|------|----------|
| 001 | VER. | SYMBOL | 2020.11.26 | QXW | 2020.11.26 | QXW | SIGN |
| | | AMENDMENT | | | | | |

| | |
|------------|---------------------|
| ME CHECKED | DRAWN |
| EE CHECKED | |
| APPROVED | CUSTOMER'S APPROVAL |

TITLE
MODULE SPEC.

DRAWING NO.
070J12757-B4 V.1

UNIT: mm SCALE: 1 OF 1 SHEET

深圳市伯朗瀚科技有限公司
Shenzhen Brownopto Technology Co.,

| NO. | SYMBOL | DESCRIPTION |
|-----|----------|-------------|
| 1 | UNIT | mm |
| 2 | SCALE | 1 OF 1 |
| 3 | SHEET | 1 OF 1 |
| 4 | DATE | 2020.11.26 |
| 5 | DESIGNER | QXW |
| 6 | APPROVED | |
| 7 | DATE | |
| 8 | SYMBOL | |
| 9 | UNIT | mm |
| 10 | SCALE | 1 OF 1 |
| 11 | SHEET | 1 OF 1 |
| 12 | DATE | 2020.11.26 |
| 13 | DESIGNER | QXW |
| 14 | APPROVED | |
| 15 | DATE | |
| 16 | SYMBOL | |
| 17 | UNIT | mm |
| 18 | SCALE | 1 OF 1 |
| 19 | SHEET | 1 OF 1 |
| 20 | DATE | 2020.11.26 |
| 21 | DESIGNER | QXW |
| 22 | APPROVED | |
| 23 | DATE | |
| 24 | SYMBOL | |
| 25 | UNIT | mm |
| 26 | SCALE | 1 OF 1 |
| 27 | SHEET | 1 OF 1 |
| 28 | DATE | 2020.11.26 |
| 29 | DESIGNER | QXW |
| 30 | APPROVED | |
| 31 | DATE | |
| 32 | SYMBOL | |
| 33 | UNIT | mm |
| 34 | SCALE | 1 OF 1 |
| 35 | SHEET | 1 OF 1 |
| 36 | DATE | 2020.11.26 |
| 37 | DESIGNER | QXW |
| 38 | APPROVED | |
| 39 | DATE | |
| 40 | SYMBOL | |
| 41 | UNIT | mm |
| 42 | SCALE | 1 OF 1 |
| 43 | SHEET | 1 OF 1 |
| 44 | DATE | 2020.11.26 |
| 45 | DESIGNER | QXW |
| 46 | APPROVED | |
| 47 | DATE | |
| 48 | SYMBOL | |
| 49 | UNIT | mm |
| 50 | SCALE | 1 OF 1 |

Backlight Circuit diagram
27LEDS

9.0 BOM LIST

(TBD)

10.0 LOT MARK

10.1 Location of Lot Mark

- (1) Location: The label is attached to the backside of the LCD module.
- (2) Detail of the Mark: as attached below.
- (3) This is subject to change without prior notice.

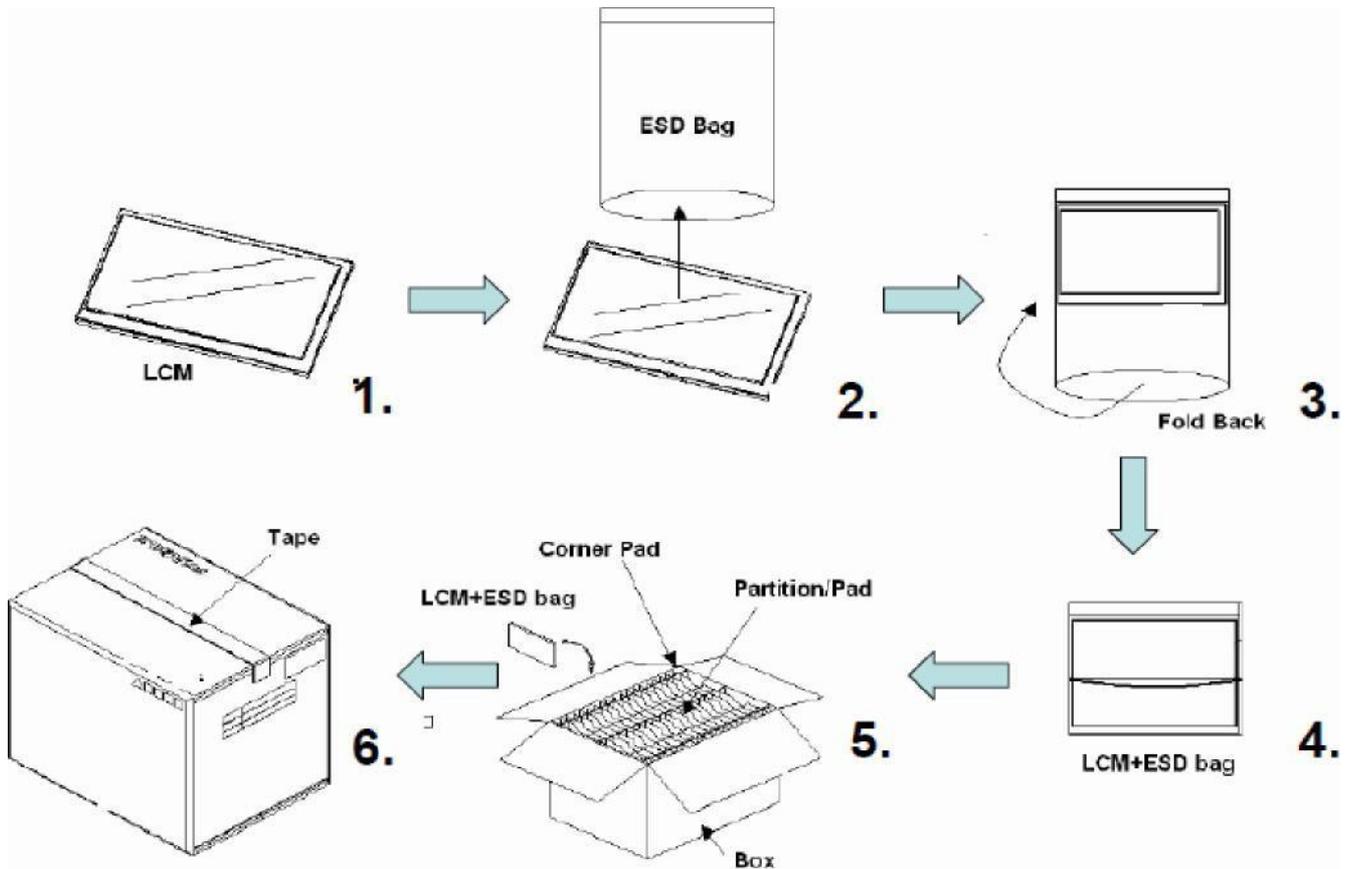


11.0 PACKAGE SPECIFICATION

11.1 Packing form

| LCM Model | LCM Qty. in the box | Inner Box Size (mm) | Note |
|---------------------|---------------------|-----------------------|------|
| 070JII2757 - B4 V.1 | 60 pcs/box | 490±5 x 340±5 x 250±5 | |

11.2 Packing assembly drawings



| Items | Material | Notice |
|---------------|------------------------|----------|
| Box | Corrugated Paper Board | AB Flute |
| Partition/Pad | Corrugated Paper Board | B Flute |
| Corner Pad | Corrugated Paper Board | AB Flute |
| ESD bag | PE | |

- 12.2.2.1 Housing case must be destined carefully so as not to put stresses on LCD all sides and not to wrench module. The stresses may cause non-uniformity even if there is no non-uniformity statically.
- 12.2.2.2 Keep sufficient clearance between LCD module back surface and housing when the LCD module is mounted. The clearance in the design is recommended taking into account the tolerance of LCD module thickness and mounting structure height on the housing.
- 12.2.3 Please do not push or scratch LCD panel surface with any-thing hard. And do not soil LCD panel surface by touching with bare hands.(Polarizer film, surface of LCD panel is easy to be flawed.)
- 12.2.4 Please do not press any parts on the rear side such as source IC, gate IC, and FPC during handling LCD module, If pressing rear part is unavoidable, handle the LCD module with care not to damage them.
- 12.2.5 Please wipe out LCD panel surface with absorbent cotton or soft cloth in case of it being soiled.
- 12.2.6 Please wipe out drops of adhesives like saliva and water on LCD panel surface immediately. They might damage to cause panel surface variation and color change.
- 12.2.7 Please do not take a LCD module to pieces and reconstruct it. Resolving and reconstructing modules may cause them not to work well.

12.3 Disassembling or Modification

Do not disassemble or modify the module. It may damage sensitive parts inside LCD module, and may cause scratches or dust on the display. Century does not warrant the module, if customers disassemble or modify the module.

12.4 Breakage of LCD Panel

- 12.4.1.If LCD panel is broken and liquid crystal spills out, do not ingest or inhale liquid crystal, and do not contact liquid crystal with skin.
- 12.4.2. If liquid crystal contacts mouth or eyes, rinse out with water immediately.
- 12.4.3. If liquid crystal contacts skin or cloths, wash it off immediately with alcohol and rinse thoroughly with water.
- 12.4.4. Handle carefully with chips of glass that may cause injury, when the glass is broken.

12.5 Absolute Maximum Ratings and Power Protection Circuit

- 12.5.1. Do not exceed the absolute maximum rating values, such as the supply voltage variation, input voltage variation, variation in parts' parameters, environmental temperature, etc., otherwise LCD module may be damaged.
- 12.5.2. Please do not leave LCD module in the environment of high humidity and high temperature for a long time.
- 12.5.3. It's recommended to employ protection circuit for power supply.

12.6 Operation

- 12.6.1 Do not touch, push or rub the polarizer with anything harder than HB pencil lead.
- 12.6.2 Use fingerstalls of soft gloves in order to keep clean display quality, when persons handle the LCD module for incoming inspection or assembly.
- 12.6.3 When the surface is dusty, please wipe gently with absorbent cotton or other soft material.
- 12.6.4 Wipe off saliva or water drops as soon as possible. If saliva or water drops contact with polarizer for a long time, they may causes deformation or color fading.

12.6.5 When cleaning the adhesives, please use absorbent cotton wetted with a little petroleum benzene or other adequate solvent.

12.7 Static Electricity

12.7.1 Protection film must remove very slowly from the surface of LCD module to prevent from electrostatic occurrence.

12.7.2. Because LCD module use CMOS-IC on circuit board and TFT-LCD panel, it is very weak to electrostatic discharge.

Please be careful with electrostatic discharge.

12.7.3 Persons who handle the module should be grounded through adequate methods.

12.8 Disposal

When disposing LCD module, obey the local environmental regulations.

12.9 Others

12.9.1 A strong incident light into LCD panel might cause display characteristics' changing inferior because of Polarizer film, color filter, and other materials becoming inferior. Please do not expose LCD module direct sunlight Land Strong UV rays.

12.9.2 Please pay attention to a panel side of LCD module not to contact with other materials in pressing it alone.

12.9.3 For the packaging box, please pay attention to the followings:

12.9.3.1 Packaging box and inner case for LCD are designed to protect the LCDs from the damage or scratching during transportation. Please do not open except picking LCDs up from the box.

12.9.3.2 Please do not pile them up more than 6 boxes(They are not designed so) And please do not turn over.

12.9.3.3 Please handle packaging box with care not to give them sudden shock and vibrations. And also please do not throw them up.

12.9.3.4 Packing box and inner case for LCDs are made of cardboard, So please pay attention not to get them wet(Such like keeping them in high humidity or wet place can occur getting them wet.)